



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

Applicant: Duane E. Allen, et al. Serial No.: 10/604142  
Filed: 6/27/03 Atty. Docket: BUR920030027US1  
Title: MASK AND SUBSTRATE ALIGNMENT FOR SOLDER BUMP PROCESS.

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.56, 1.97, 1.98**

Honorable Commissioner of Patents and Trademarks  
Washington, D. C. 20231

Sir:

Applicants submit herewith form PTO-1449, listing patents, publications, or other information of which they are aware which they believe may be material to patentability pursuant to 37 CFR 1.56(b), and in respect of which there may be a duty to disclose under 37 CFR 1.56(a), together with legible copies of the patents, publications, or other information listed.

While the items submitted with this Information Disclosure Statement may be material to patentability pursuant to 37 CFR 1.56, in accordance with 37 CFR 1.97(h) it shall not be construed to be an admission that any patent, publication, or other information cited is "prior art" or is material to patentability for this invention unless specifically designated as such. In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other information material to patentability, as defined in 37 CFR 1.56(b), exists.

Date: Jul. 16, 2003

Respectfully submitted,  
By:

  
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<b>INFORMATION DISCLOSURE CITATION</b> <small>(Use several sheets if necessary)</small>				ATTY DOCKET NO. <b>BUR920030027US1</b>		SERIAL NO. <b>10/1004142</b>	
				Duane E. Allen et al.		FILING <b>6/27/03</b>	
<b>U.S. PATENT DOCUMENTS</b>							
<small>*EXAMINER &amp; TRADEMARK OFFICE</small> INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<b>FOREIGN PATENT DOCUMENTS</b>							
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
					YES	NO	
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
/AS/			IBM Technical Disclosure Bulletin, MASK/WAFER LOCKING DEVICE, Vol. 21, No. 11, April 1979, DiMilia et al., pages 4684-4685.				
/AS/			IBM Technical Disclosure Bulletin, MASK ALIGNING FIXTURE FOR SILICON WAFERS, Vol. 27, No. 4B, September 1984, Christensen et al., pages 2383-2384.				
<b>EXAMINER</b> /Andre Stevenson/ (03/26/2008)			<b>DATE CONSIDERED</b> 03/26/2008				

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**DATE CONSIDERED**  
03/26/2008

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.